

Day : Wednesday

Date: 9/28/2005

Time: 16:04:56

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = KUNKEL

First Name = GERHARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#"><u>09161861</u></a>	<a href="#"><u>6136660</u></a>	150	09/28/1998	STACKED CAPACITATOR MEMORY CELL AND METHOD OF FABRICATION	KUNKEL, GERHARD
<a href="#"><u>09163658</u></a>	<a href="#"><u>Not Issued</u></a>	161	09/30/1998	DYNAMIC RANDOM ACCESS MEMORY LAYOUT	KUNKEL, GERHARD
<a href="#"><u>09250861</u></a>	<a href="#"><u>Not Issued</u></a>	164	02/17/1999	METHOD OF IMPROVING THE ETCH RESISTANCE OF PHOTORESISTS	KUNKEL, GERHARD
<a href="#"><u>09272215</u></a>	<a href="#"><u>6211544</u></a>	150	03/18/1999	MEMORY CELL LAYOUT FOR REDUCED INTERACTION BETWEEN STORAGE NODES AND TRANSISTORS	KUNKEL, GERHARD
<a href="#"><u>09277669</u></a>	<a href="#"><u>6083788</u></a>	150	03/26/1999	STACKED CAPACITOR MEMORY CELL AND METHOD OF MANUFACUTRE	KUNKEL, GERHARD
<a href="#"><u>09282745</u></a>	<a href="#"><u>6379869</u></a>	150	03/31/1999	METHOD OF IMPROVING THE ETCH RESISTANCE OF CHEMICALLY AMPLIFIED PHOTORESISTS BY INTRODUCING SILICON AFTER PATTERNING	KUNKEL, GERHARD
<a href="#"><u>09407437</u></a>	<a href="#"><u>6118683</u></a>	150	09/29/1999	DYNAMIC RANDOM ACCESS MEMORY CELL LAYOUT	KUNKEL, GERHARD
<a href="#"><u>09408246</u></a>	<a href="#"><u>6590657</u></a>	150	09/29/1999	SEMICONDUCTOR STRUCTURES AND MANUFACTURING METHODS	KUNKEL, GERHARD
<a href="#"><u>09460700</u></a>	<a href="#"><u>6420272</u></a>	150	12/14/1999	METHOD FOR REMOVAL OF HARD MASK USED TO DEFINE NOBLE METAL ELECTRODE	KUNKEL, GERHARD
<a href="#"><u>09522883</u></a>	<a href="#"><u>6365328</u></a>	150	03/10/2000	SEMICONDUCTOR	KUNKEL,

				STRUCTURE AND MANUFACTURING METHOD	GERHARD
<u>09561469</u>	6511791	150	04/28/2000	MULTIPLE EXPOSURE PROCESS FOR FORMATION OF DENSE RECTANGULAR ARRAYS	KUNKEL, GERHARD
<u>09561472</u>	6451508	150	04/28/2000	PLURAL INTERLEAVED EXPOSURE PROCESS FOR INCREASED FEATURE ASPECT RATIO IN DENSE ARRAYS	KUNKEL, GERHARD
<u>09561581</u>	Not Issued	161	04/28/2000	Group connection of reticle sub- shapes to reduce line shortening and improve pattern fidelity	KUNKEL, GERHARD
<u>09597116</u>	Not Issued	161	06/20/2000	Buried bit line-shallow trench isolation defined active semiconductor areas	KUNKEL, GERHARD
<u>09597123</u>	6727989	150	06/20/2000	ENHANCED OVERLAY MEASUREMENT MARKS FOR OVERLAY ALIGNMENT AND EXPOSURE TOOL CONDITION CONTROL	KUNKEL, GERHARD
<u>09598789</u>	6911687	150	06/21/2000	Buried bit line-field isolation defined active semiconductor areas	KUNKEL, GERHARD
<u>09602717</u>	6383691	150	06/26/2000	Photomask and method for increasing image aspect ratio while relaxing mask fabrication requirements	KUNKEL, GERHARD
<u>09603337</u>	6282116	250	06/26/2000	Dynamic random access memory	KUNKEL, GERHARD
<u>09609472</u>	Not Issued	161	06/30/2000	CONCAVE SHAPED STACK CAPACITOR	KUNKEL, GERHARD
<u>09801413</u>	6558883	150	03/08/2001	APPARATUS AND METHOD FOR PATTERNING A SEMICONDUCTOR WAFER	KUNKEL, GERHARD
<u>09916917</u>	6606151	150	07/27/2001	GRATING PATTERNS AND METHOD FOR DETERMINATION OF AZIMUTHAL AND RADIAL ABERRATION	KUNKEL, GERHARD
<u>09957937</u>	6566219	150	09/21/2001	METHOD OF FORMING A SELF ALIGNED TRENCH IN A SEMICONDUCTOR USING A PATTERNEDE SACRIFICIAL LAYER FOR DEFINING THE	KUNKEL, GERHARD

				TRENCH OPENING	
<a href="#"><u>10406888</u></a>	6842222	150	04/04/2003	METHOD OF REDUCING PITCH ON SEMICONDUCTOR WAFER	KUNKEL, GERHARD
<a href="#"><u>10424039</u></a>	6809800	150	04/25/2003	APPARATUS FOR PATTERNING A SEMICONDUCTOR WAFER	KUNKEL, GERHARD
<a href="#"><u>10487911</u></a>	Not Issued	30	08/13/2004	Photolithographic mask	KUNKEL, GERHARD
<a href="#"><u>10637193</u></a>	Not Issued	30	08/08/2003	Method for the characterization of an illumination source in an exposure apparatus	KUNKEL, GERHARD
<a href="#"><u>10787118</u></a>	Not Issued	30	02/27/2004	Phase-shift mask	KUNKEL, GERHARD
<a href="#"><u>10870699</u></a>	Not Issued	30	06/17/2004	Method for forming an opening on an alternating phase shift mask	KUNKEL, GERHARD
<a href="#"><u>10965693</u></a>	Not Issued	30	10/14/2004	Method for producing a mask adapted to an exposure apparatus	KUNKEL, GERHARD
<a href="#"><u>11056402</u></a>	Not Issued	20	02/14/2005	Method for producing a phase mask	KUNKEL, GERHARD
<a href="#"><u>11065931</u></a>	Not Issued	20	02/25/2005	Run to run control for lens aberrations	KUNKEL, GERHARD
<a href="#"><u>11066638</u></a>	Not Issued	20	02/25/2005	System for analyzing images of blazed phase grating samples	KUNKEL, GERHARD
<a href="#"><u>11066913</u></a>	Not Issued	30	02/25/2005	Optimizing focal plane fitting functions for an image field on a substrate	KUNKEL, GERHARD
<a href="#"><u>11067191</u></a>	Not Issued	20	02/25/2005	Optimizing light path uniformity in inspection systems	KUNKEL, GERHARD
<a href="#"><u>11170189</u></a>	Not Issued	20	06/29/2005	Method for adapting structure dimensions during the photolithographic projection of a pattern of structure elements onto a semiconductor wafer	KUNKEL, GERHARD
<a href="#"><u>60234502</u></a>	Not Issued	159	09/22/2000	Self aligned trench and method of forming the same	KUNKEL, GERHARD

Inventor Search Completed: No Records to Display.

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